



PTO/SB/08A (04-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	101609-157
Filing Date	06-30-2003
First Named Inventor	Hai Jiang
Art Unit	2824
Examiner Name	
Attorney Docket Number	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
PL	A	US- 5,825,046	10-20-1998	Wolodymyr Czubytyj	
PL	B	US- US2003/0116794A	06-26-2003	Tyler A. Lowrey	
PL	C	US- US2002/0123170A	09-05-2002	John T. Moore	
PL	D	US- US2003/0146452A	08-07-2003	Chien Chiang	
PL	E	US- 6,075,719	06-13-2000	Tyler Lowrey	
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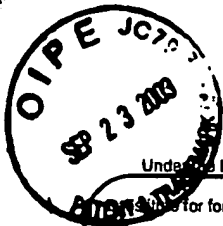
FOREIGN PATENT DOCUMENTS						
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Sheet	9	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PL	F	J. N. Zhou, etc, "Thickness dependence of magnetic percolation threshold in as-deposited	
PL		and annealed Fe-SiO ₂ granular very thin films", J. Appl. Phys. 84, (1998)5693	
PL	G	S. Lai, etc, "OUM - a 180 nm non-volatile memory cell element technology for stand	
PL		alone and embedded applications", http://www.intel.com/research/silicon/flash.htm	

Examiner Signature	PHO LUY	Date Considered	6/6/05
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